Call For Papers

IT-NEWS The 4th International Telecommunication NEtworking Workshop on **QoS in Multiservice IP Networks (QoS-IP 2008)**

Venezia, Italy
February 13-15, 2008

http://www.tlc-networks.polito.it/QoS-IP2008/

All-IP has become the dominant networking paradigm for the present century. The evolution towards an all-IP core network infrastructure continues unabated, with an enriched IP layer often implementing the unique networking functionalities on top of WDM links. Moreover, IP over switched Giga (or 10-Giga) Ethernet is replacing the traditional, relatively slow-speed, access means. Finally, the widespread deployment of non-3G wireless access networking technologies (such as 802.11 WiFi wireless LANs or 802.16 WiMax wireless MANs) requires IP to take roles traditionally envisioned for 3G core networks.
Thanks to the IP flexibility, the management of traffic flows generated by a variety of applications (web browsing, e-mail, telephony, audio and video distribution, multimedia multicasting, financial transactions, distributed gaming, domotics, disaster recovery and business continuity,...), whose performance requirements are extremely different, has become possible. This situation has generated a great interest in the development of techniques for the provision of quality of service (QoS) guarantees in IP networks offering a variety of services through a range of different user interfaces, in the development of mapping strategies between the end-to-end QoS models provided at the IP layer and the local QoS solutions offered by the underlying wired/wireless networking technologies, and in the development of tools for network optimization and control.
However, the dramatically increasing size of the Internet on the one hand, and the development of new transmission technologies that continuously drive transmission speed increase on the other hand, have raised a number of scalability issues such as the inability to tightly control QoS requirement if not for highly aggregated flows, the difficulty in scaling switch and router size to keep up with increasing link transmission speed and number of interfaces, the need to develop new models for traffic management of emerging popular applications (such as those based on the peer-to-peer paradigm), and the planning and control of large-size heterogeneous networks.
In 2006 and 2007, the Italian Ministry for University and Research has been funding four research program in networking, named BORA-BORA (on software routers), OSATE (on optics in switching), MIMOSA (on traffic measurements and modeling), and FAMOUS (on modeling peer-to-peer architectures). QoS-IP 2008 is jointly organized by these projects, and aims at the presentation of high-quality recent research results in networking , at the dissemination of the most relevant research results obtained within those projects, and at providing an international forum for sharing research experiences related to QoS in IP networks.

Contributions are invited on all topics concerning QoS guarantees in multiservice IP networks, including:

- Techniques for the provision of QoS guarantees in IP networks
- End-to-end QoS in IP networks
- Adaptive and customizable QoS provision
- Network planning and optimization for QoS
- Traffic models for QoS network design
- Analytical and simulation models for QoS estimation
- Fluid models for QoS estimation in large IP networks
- Experimental results in QoS
- Measurement and traffic detection or identification
- Scalable switching architectures for QoS
- Software routers
- Optical packet/burst switching
- QoS support in switch architectures
- QoS in wireless/mobile IP networks
- QoS in photonic IP networks
- QoS for special environments (Peer-to-peer, infomobility, broadcasting, ...)

Call For Papers

Important Deadlines

- June 29th, 2007 — Electronic Paper Submission
- October 1st, 2007 — Acceptance Notification
- November 1st, 2007 — Final Version Due

Author guidelines

Papers must be written in English, should not exceed 5000 words and should be submitted according to the IEEE double-column format, as compressed files in PDF format, through the EDAS web page http://edas.info/. Papers must be unpublished and must not be submitted for publication elsewhere.

Organizing Committee

General Chair
Fabio Neri, Politecnico di Torino Italy

Program Committee Co-chairs
Andrea Bianco, Politecnico di Torino, Italy
Alfio Lombardo, Università di Catania, Italy
Achille Patavina, Politecnico di Milano, Italy

Publications/Publicity Chair
Guido Maier, Politecnico di Milano, Italy

Information System Chair
Maurizio M. Munafò, Politecnico di Torino, Italy

Technical Program Committee

Yan Akyldiz, GeorgiaTech, USA
Nirwan Ansari, NJIT, USA
Javier Aracil, Universidad Autonoma de Madrid, Spain
Mohammed Atiquzzaman, Univ. of Oklahoma, USA
Tulin Atmaca, INT Evry, France
Andrea Baiocchi, Univ. of Roma La Sapienza, Italy
Mario Baldi, Politecnico di Torino, Italy
Giuseppe Bianchi, Roma Tor Vergata, Italy
Ernst Biersack, Eurecom, France
Franco Callegati, Univ. of Bologna, Italy
Davide Careglio, UPC, Spain
Augusto Casaca, INESC, Portugal
Giorgio Corazza, Univ. of Bologna, Italy
Rene Cruz, Univ. of California San Diego, USA
Franco Davoli, Univ. of Genova, Italy
Edmundo A. de Souza e Silva, UFRJ, Brazil
Tien Van Do, BUTE, Hungary
Josè Enrìquez-Gabeiras, Telefònica, Spain
Viktoria Fodor, KHT, Sweden
Luigi Fratta, Politecnico di Milano, Italy
Andrea Fumagalli, Univ. of Texas Dallas, USA
Rossano Gaeta, Univ. of Torino, Italy
Paolo Giaccone, Politecnico di Torino,
Stefano Giordano, Univ. of Pisa, Italy
Weibo Gong, UMass, USA
Annie Gravey, ENST-Bretagne, France
Mounir Hamdi, HKUST, Hong Kong
Boudewijn Haverkort, Univ. of Twente, The Netherlands
Xiaohong Jiang, Tohoku Univ., Japan
Laszlo Jereb, BUTE, Hungary
Carlos Juiz, Universitat de les Illes Balears, Spain
Admela Jukan, Uni. of Illinois Urbana Champaign, USA
Wojciech Kabacinski, Poznan Univ., Poland
T. V. Lakshman, Lucent Bell Labs, USA
Renato Lo Cigno, Univ. of Trento, Italy
D. Manjunath, Indian Institute of Technology, Bombay
Saverio Mascolo, Politecnico di Bari, Italy
Xavier Masip-Bruin, UPC, Spain
Marco Mellia, Politecnico di Torino, Italy
Sandor Molnar, BUTE, Hungary
Masayuki Murata, Osaka Univ., Japan
Ilkka Norros, VTT Information Technology, Finland
Sara Oueslati, France Telecom R&D, France
Elena Pagani, Univ. of Milano, Italy
Sergio Palazzo, Univ. of Catania, Italy
Krzysztof Pawlikowski, Univ. of Canterbury, New Zealand
Xavier Perez-Costa, NEC, Germany
Guy Pujolle, Univ. Paris VI, France
Carla Raffaelli, Univ. of Bologna, Italy
Roberto Rojas-Cessa, NJIT, USA
Dario Rossi, ENST, Paris, France,
Gian Paolo Rossi, Univ. of Milano, Italy
Roberto Sabella, Ericsson, Italy
Stefano Salsano, Univ. of Roma Tor Vergata, Italy
Matteo Sereno, Univ. of Torino, Italy
Dimitris Serpanos, Univ. of Patras, Greece
Aleksandra Smiljanic, Belgrade Univ., Serbia
Josep Sole Pareta, UPC, Spain
Dimitris Stiliadis, USA
Heinrich Stuettgen, NEC, Germany
Tatsuya Suda, UCI, USA
Yutaka Takahashi, Kyoto Univ., Japan
Tetsuya Takine, Osaka Univ., Japan
Phuoc Tran-Gia, Univ. of Würzburg, Germany
Kurt Tutschku, Univ. of Würzburg, Germany
Guillaume Urvoy-Keller, Eurecom, France
Giorgio Ventre, Univ, of Napoli, Italy
Adam Wolisz, TU Berlin, Germany
Yang Yang, UCL, UK
Tatsuya Suda, UCI, USA
Guillaume Urvoy-Keller, Eurecom, France
Nina Taft, Intel Research, USA
Yutaka Takahashi, Kyoto University, Japan
Tetsuya Takine, Kyoto University, Japan
Asser Tantawi, IBM, USA
Phuoc Tran-Gia, University of Würzburg, Germany
Kurt Tutschku, University of Würzburg, Germany
Adam Wolisz, TU Berlin, Germany
Bulent Yener, RPI, USA